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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known				
Application Number	09/601,122-Conf. #5606			
Filing Date	February 23, 2001			
First Named Inventor	Graham Alexander Munro Murdoch			
Art Unit	2618			
Examiner Name	E. Yun			
Attorney Docket Number	61229-00005USPX			

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where
		Number-Kind Code <sup>2</sup> (if known)			Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document  Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>4</sup> (il known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T°

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	C1 ~	SKILLING, H. H., "Electrical Engineering Circuits", 2nd edition, pages 373-375		

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